

In the Specification

Please replace the paragraph beginning at page 1, line 4 with the following entry:

This application is a continuation of U.S. Patent Application No. [09/516,961]09/515,961 (Atty. Docket No. 60178.300401) entitled Low Voltage Rail-to-Rail CMOS Output Stage, filed on 02/29/2000, which subsequently matured into U.S. Patent No. 6,353,363 on 3/5/02. This application is related to U.S. Patent Application No. 09/516,008 (Atty. Docket No. 60178.300301) entitled Low Voltage Rail-to-Rail CMOS input stage, filed on an even day herewith on behalf of Troy Stockstad, the disclosure of which is incorporated herein by reference.